Notice of References Cited

Application/Control No. 10/816,063	Applicant(s)/Patent Under Reexamination YAMAMOTO ET AL.		
Examiner	Art Unit		
Brian T. Misiura	2112	Page 1 of 1	

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	Α	US-2003/0038177	02-2003	Morrow, Neil	235/441
*	В	US-2003/0172263	09-2003	Liu, Wen-Tsung	713/156
*	C	US-2004/0059860	03-2004	Liu et al.	710/313
*	D	US-2004/0133712	07-2004	Yamamoto et al.	710/023
*	Е	US-2005/0198399	09-2005	Stancil, Charles J.	709/249
*	F	US-2005/0233643	10-2005	Tsay et al.	439/628
*	G	US-6,438,638	08-2002	Jones et al.	710/301
*	Н	US-6,470,284	10-2002	Oh et al.	702/64
*	1	US-6,684,283	01-2004	Harris et al.	710/302
*	J	US-6,718,274	04-2004	Huang et al.	702/64
*	К	US-6,970,964	11-2005	Mowery et al.	710/302
	L	US-			·
	М	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	0					
	Р					
	Q					
	R					
	S		•			
	Т					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)			
*	U	PCMCIA PC Card Bus Description, PC Card Pinout and Signal Names - "PCMCIA 16 bit Bus" - available at "http://www.interfacebus.com/Design_Connector_PCMCIA.html"			
*	٧	PCMCIA CardBus Description, PinOut and Signal Names - "PCMCIA 32 bit Bus" - available at "http://www.interfacebus.com/Design_Connector_Cardbus.html"			
*	w	PCMCIA ExpressCard Description, PinOut and Signal Names - "PCMCIA ExpressCard" - available at "http://www.interfacebus.com/Design_Connector_ExpressCard_Bus.html"			
*	x	"ExpressCard: Superfast I/O" - available at "http://www.pcmag.com/article2/0,4149,1472734,00.asp" - dated 2/17/2004			

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).) Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.